

第八届塑料技术在电子产品中的应用研讨会

导热绝缘电子封装材料研究新进展

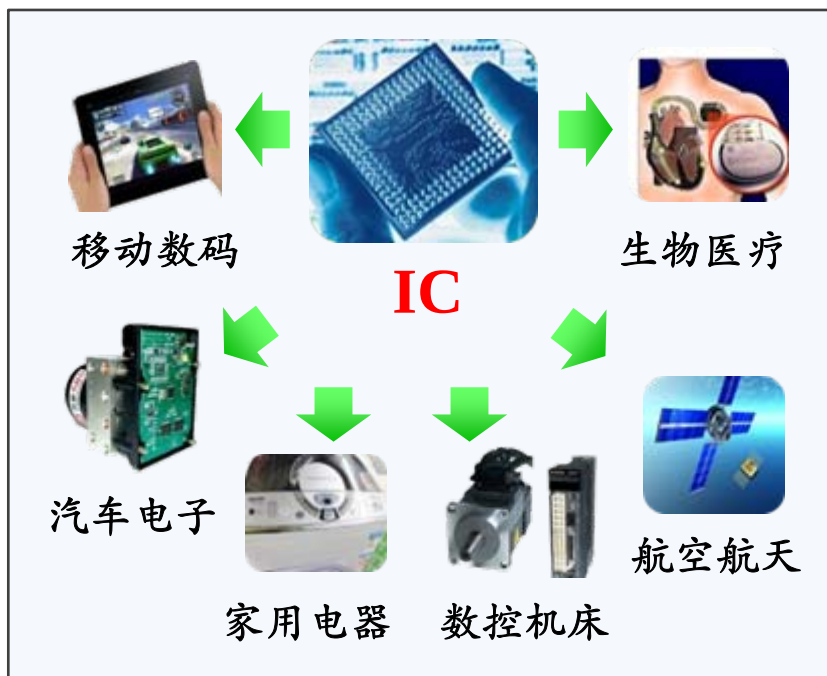
周兴平

华中科技大学化学与化工学院
材料成形与模具技术国家重点实验室

2013-5-8重庆

研究背景

微电子是世界支柱产业，**集成电路 (IC) 芯片**是基石



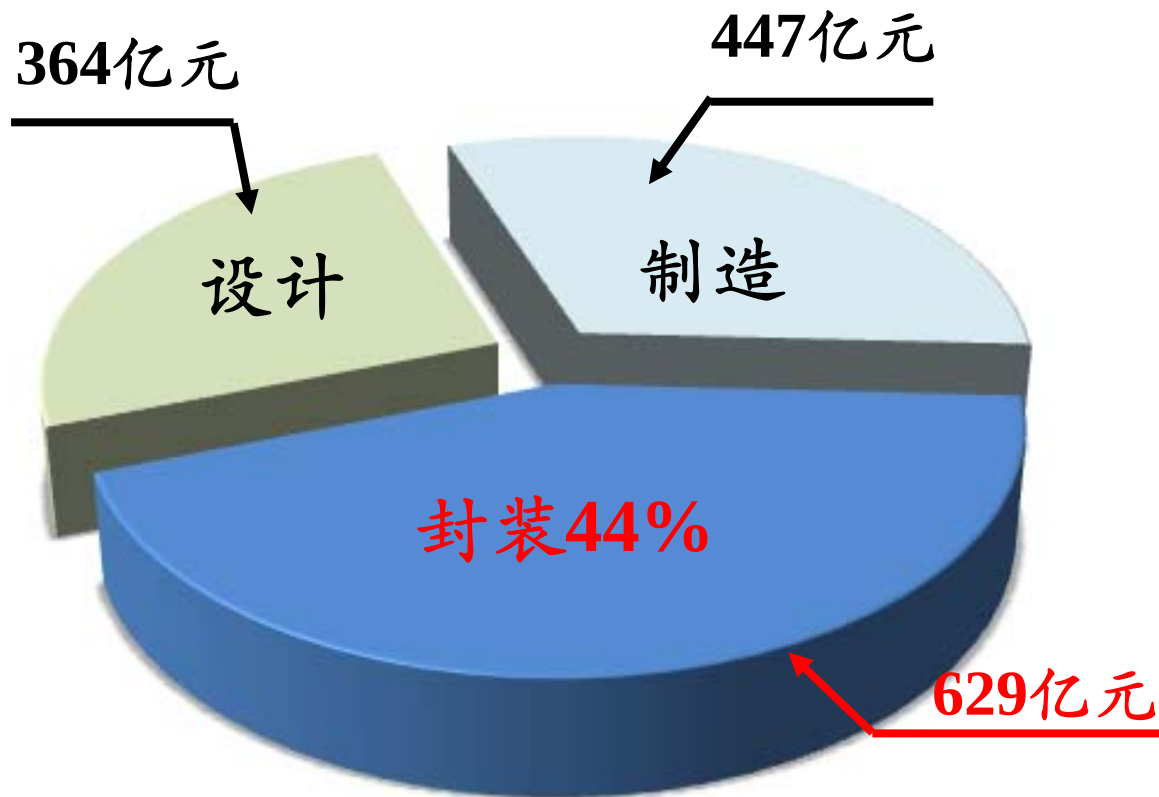
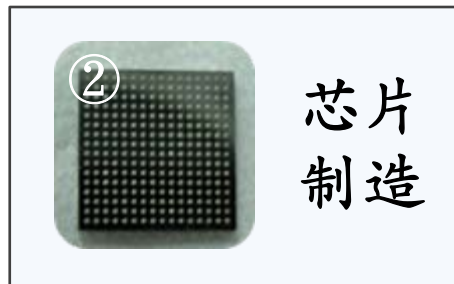
IC的广泛应用



2010年我国进口数据

IC芯片是我国进口金额最大的单宗商品

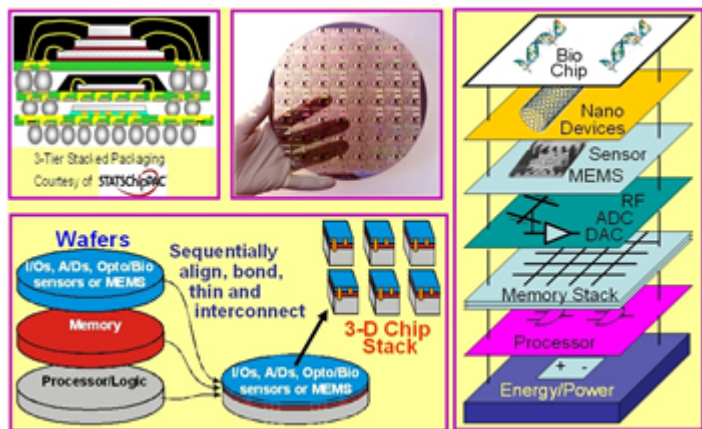
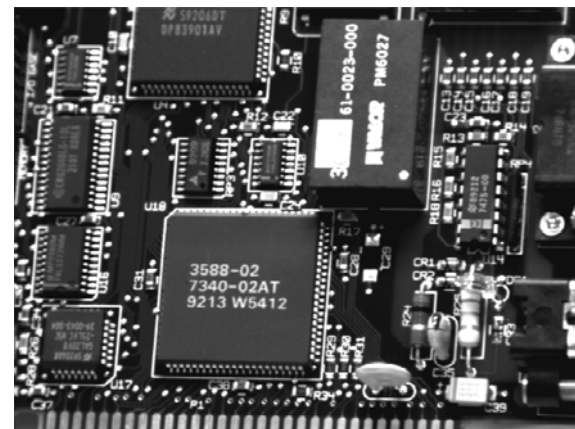
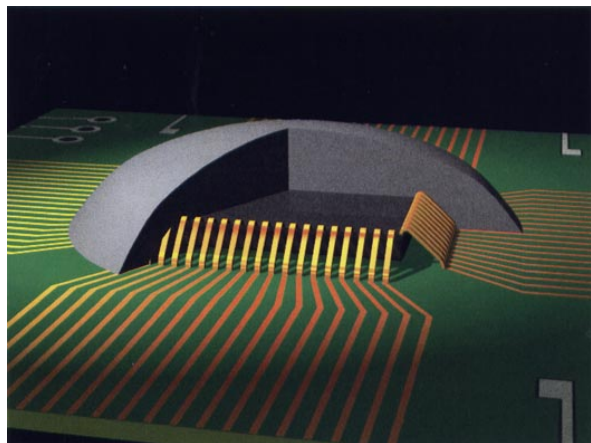
电子封装是IC产业链的关键环节，占IC产业总值的44%



数据来源：中国IC产业报告

2010年我国IC产业链产值构成

电子封装



- 提供信号输入、输出通路;
- 提供热扩散通路
- 提供电流通路
- 提供机械支撑和环境保护

Deborah D.L.Chung. Materials for Electronic Packaging. Elsevier, Newton,USA,1995

电子封装是把电子元器件或集成电路各部件按要求合理布置、组装、键合、连接起来的操作过程,防止水分、尘埃及有害气体对器件、电路的影响。

电子封装热管理



Products Technology Communities Downloads Reseller

Technology

Architecture & Silicon Technology

Microarchitecture

Silicon technology

Multi-core technology

Another World's First.

Introducing 32nm logic technology.
Only from Intel.

Home > Technology > Architecture & Silicon > Silicon >
32nm Logic Technology

Revolutionizing How We Use Technology—Today and Beyond

In another world's first, Intel has demonstrated its 32nm logic process with a functional SRAM packing more than 1.9 billion second generation high-k metal gate transistors. It's a monumental step towards delivering 32nm microprocessors in 2009—and a great leap towards developing significant density, performance, and power improvements beyond today's 45nm technology.

Breakthroughs announced at ID

Get the details on generation 45nm I microarchitecture.



2010年

Intel新一代32nm制程
六核CPU 具有19亿个晶体管

Intel Tick-Tock

Innovation driven by manufacturing process and microprocessor advances

PERFORMANCE
NEW CAPABILITIES
ENERGY EFFICIENCY
FORM FACTOR ADVANCES

65nm

Intel® Core™
micro-
architecture

45nm

Intel® micro-
architecture
codename
Nehalem

32nm

Intel® micro-
architecture
codename
Sandy Bridge

22nm

2006

2008

2010

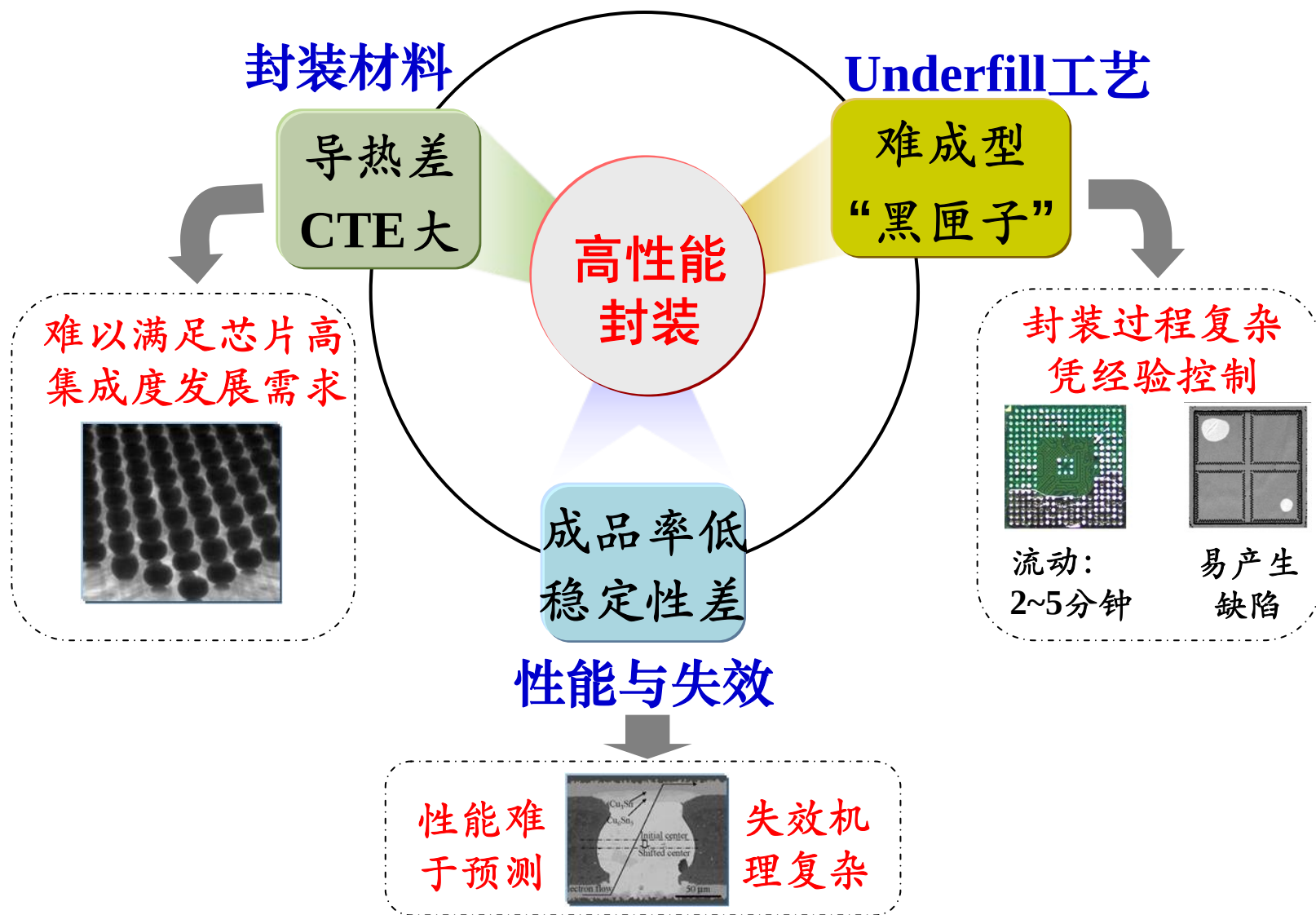
2012

热扩散问题!!

制约电子封装领域的瓶颈问题

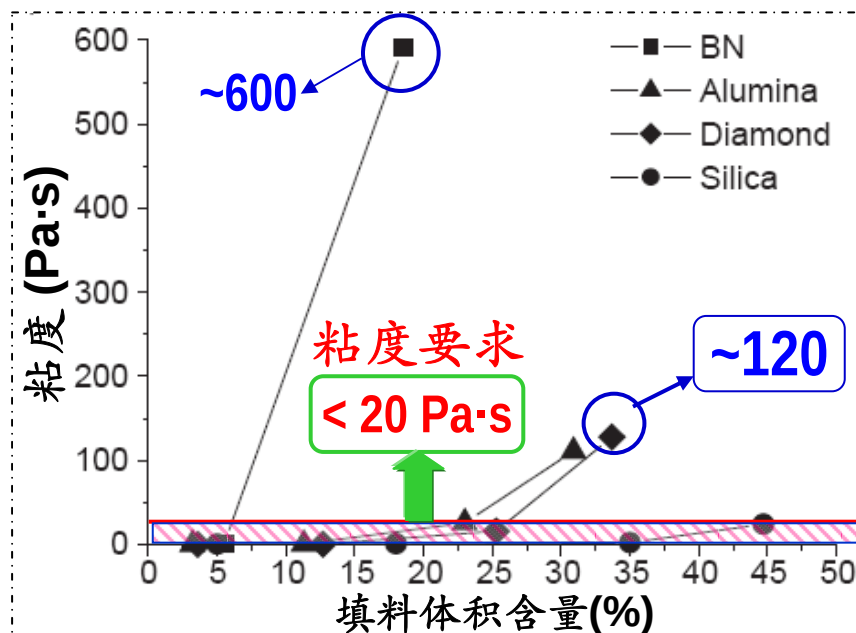
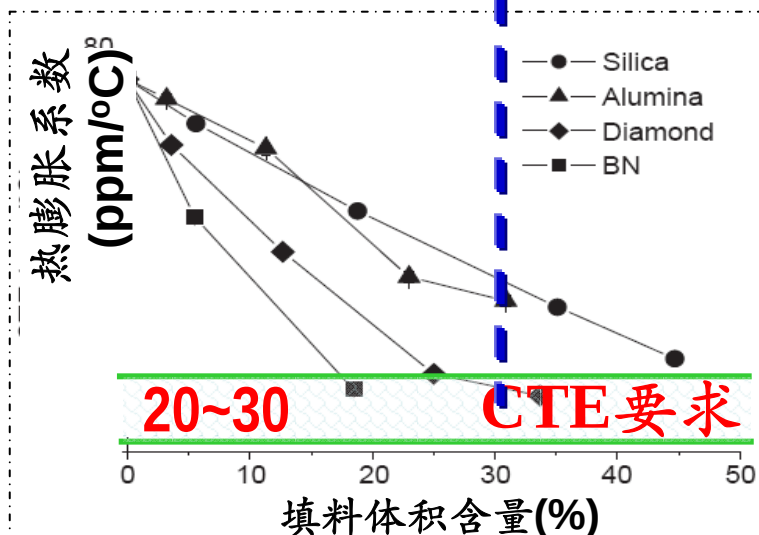
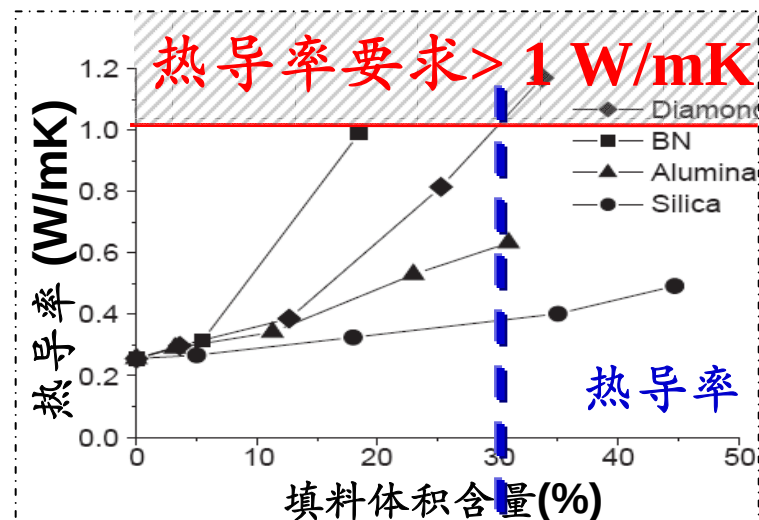
www.intel.com

电子封装难题



环氧封装材料研究现状

添加导热无机填料是常用方法



高填充量导致体系粘度高，
不能满足Underfill封装工艺

填料热学性质

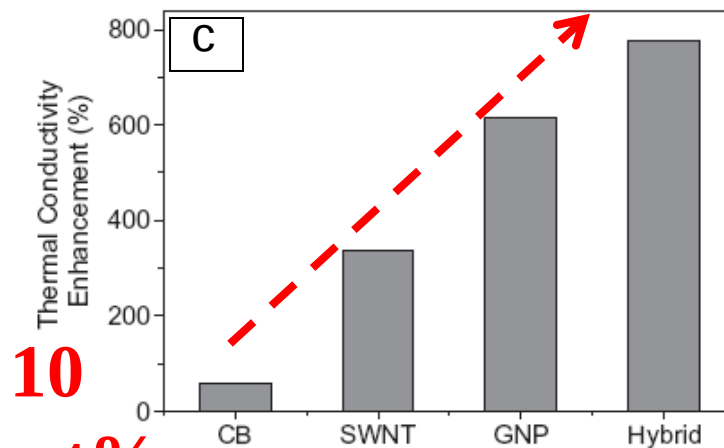
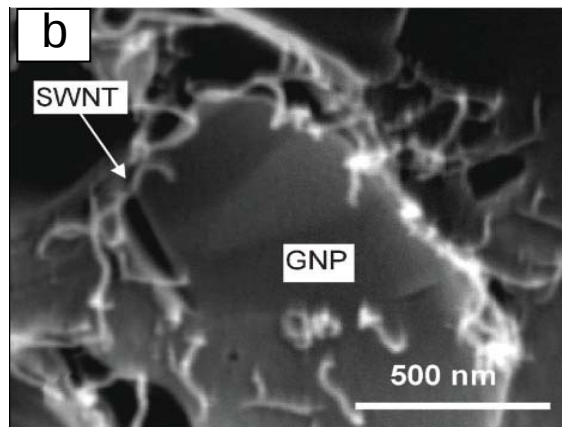
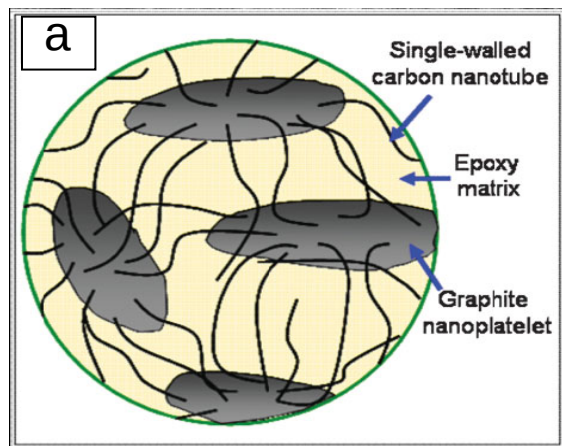
材料	热导率 W/m K	CTE ppm/°C
环氧	0.14	75.2
SiO ₂	1.5	0.5
Al ₂ O ₃	36	6.6
BN	250~300	<0.5
碳纳米管	> 3000	→0
石墨烯	> 4800	→0

存在的问题

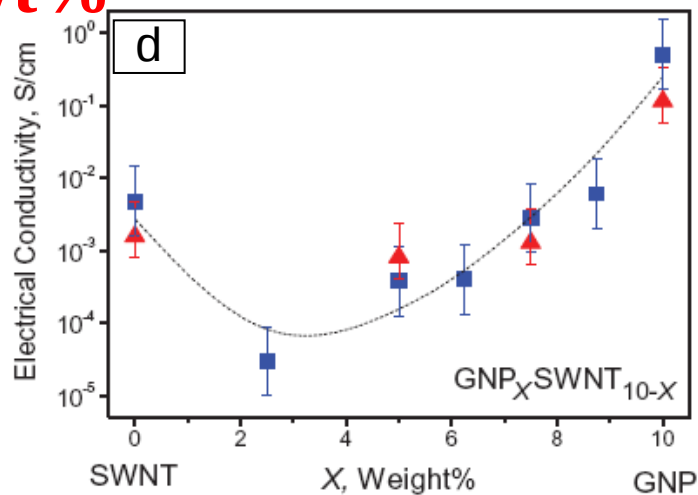
- 填料导电
- 复合体系粘度高
- 复合界面热阻大

理想的电子封装用导热填料

添加碳管或石墨烯等方法

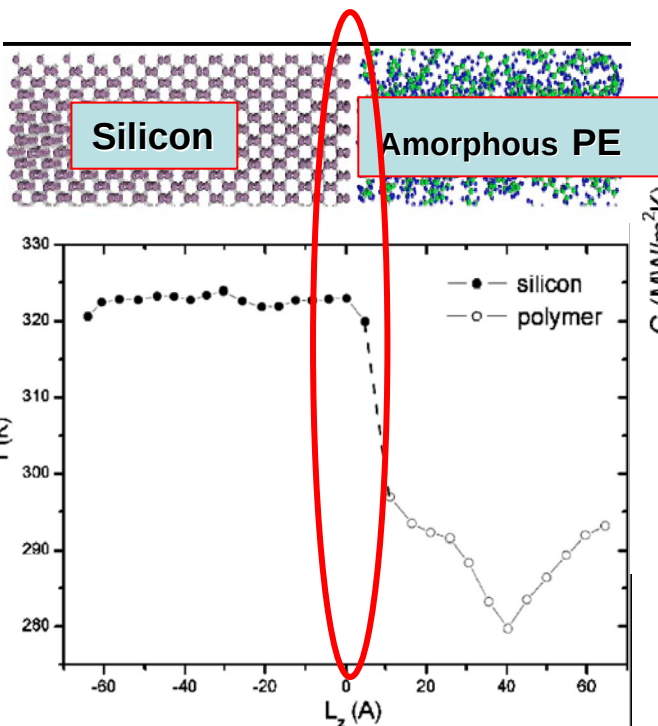


10
wt%

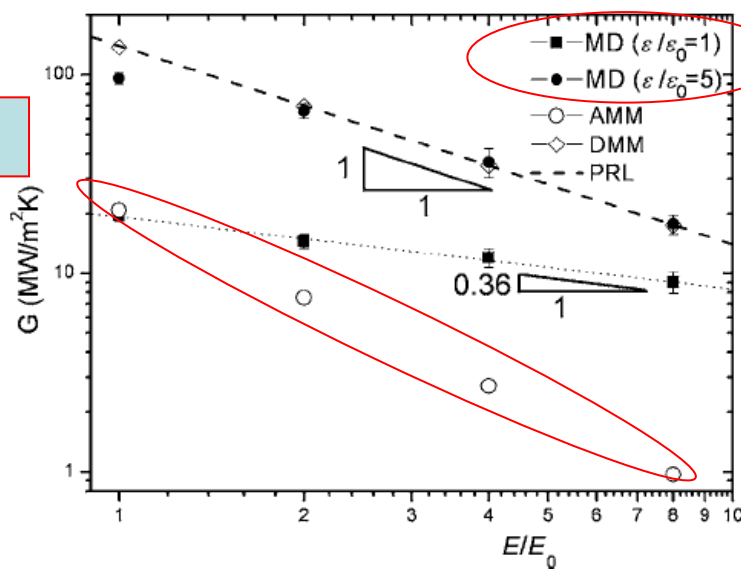


导热大幅增加
但绝缘性破坏

热界面热阻

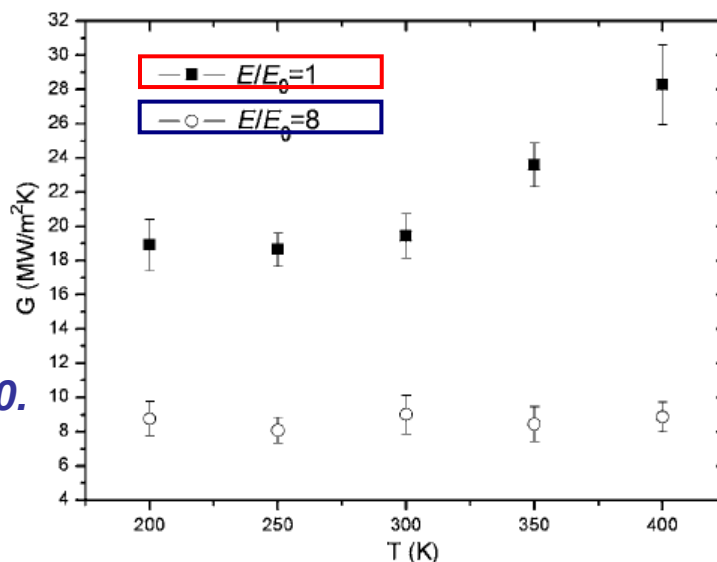


热界面热阻使得温度不连续



单晶硅/聚乙烯界面热导随硅模量 E 及单晶硅与聚乙烯间相互作用力 ϵ 的变化关系,
 E_0 为硅的基准模量,
 ϵ_0 为基准范德华力

Bonding Strength



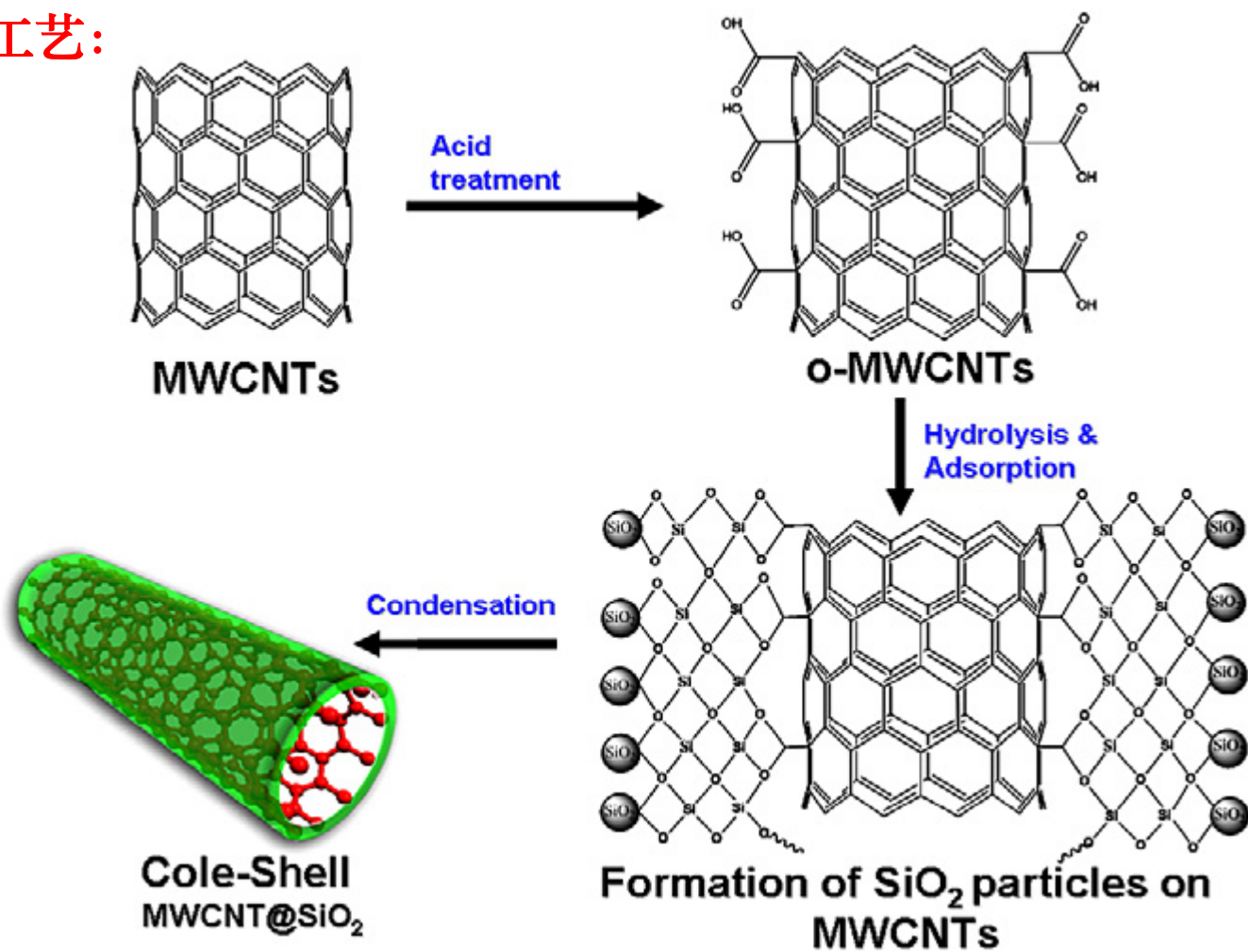
单晶硅/聚乙烯 (■)
金刚石/聚乙烯 (○)
的界面热导随温度的变化关系

Modulus Mismatch

Appl. Phys. Lett. 2007, 91,241910.
Phys. Rev.B,2009,79,104305.

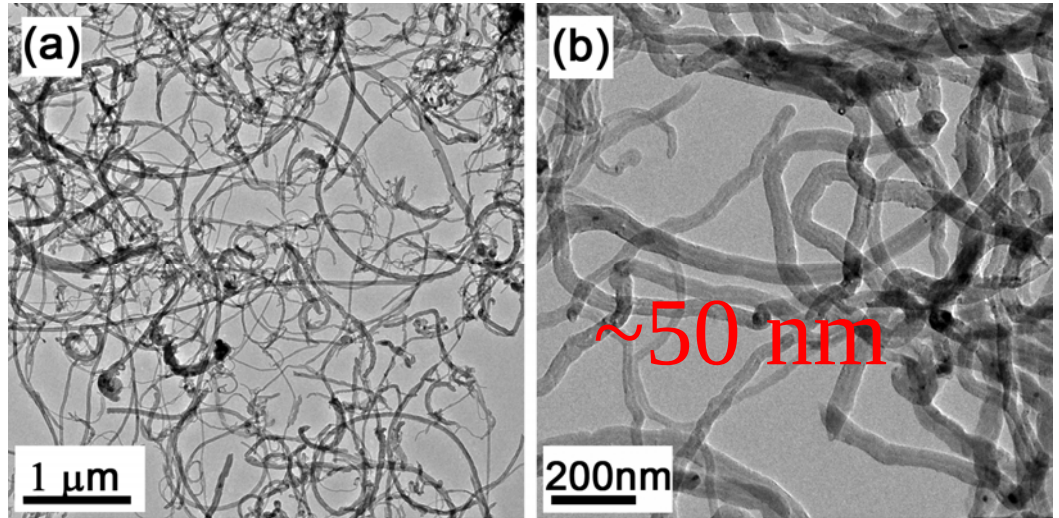
体系1： 二氧化硅包覆MWCNT

Sol-Gel 工艺:

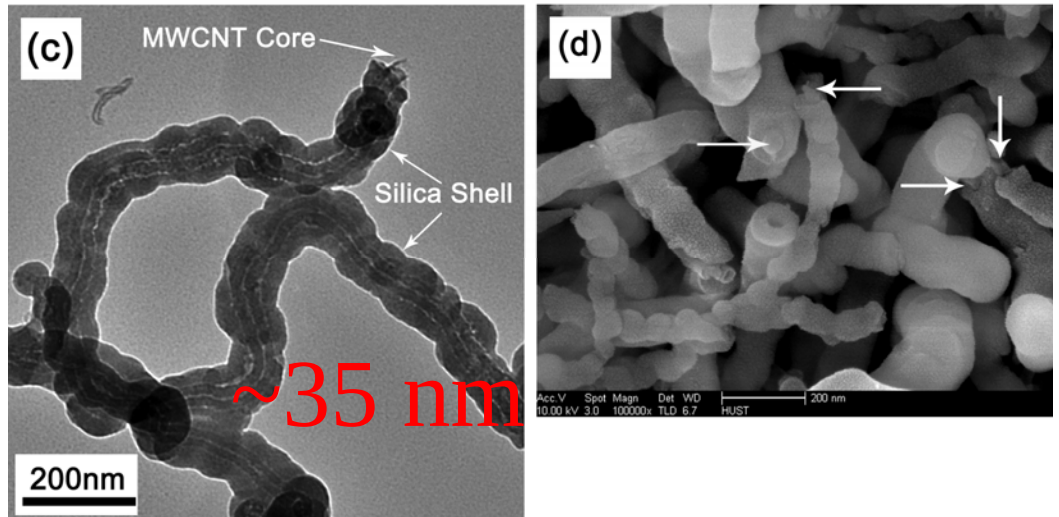


二氧化硅包覆MWCNT形貌

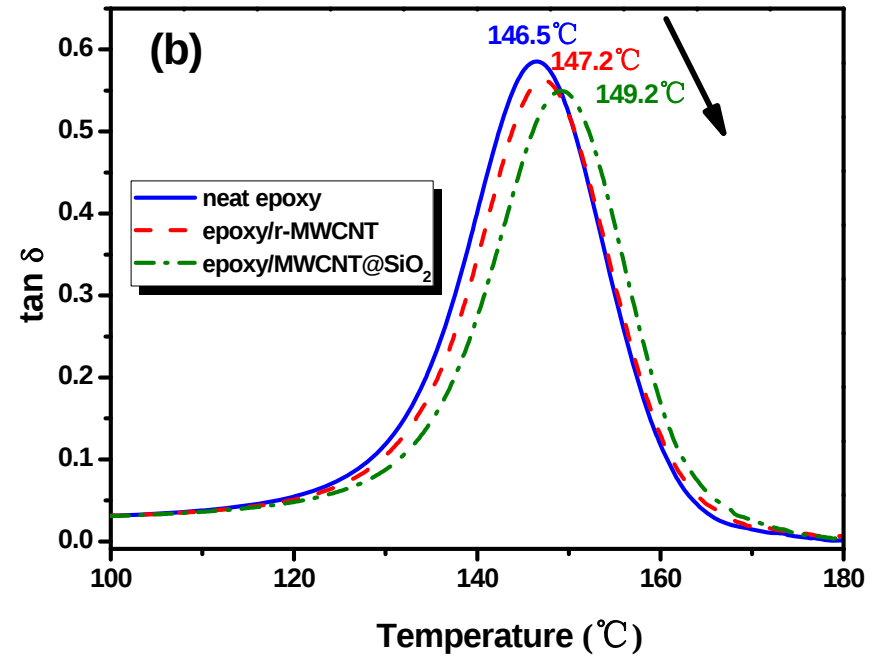
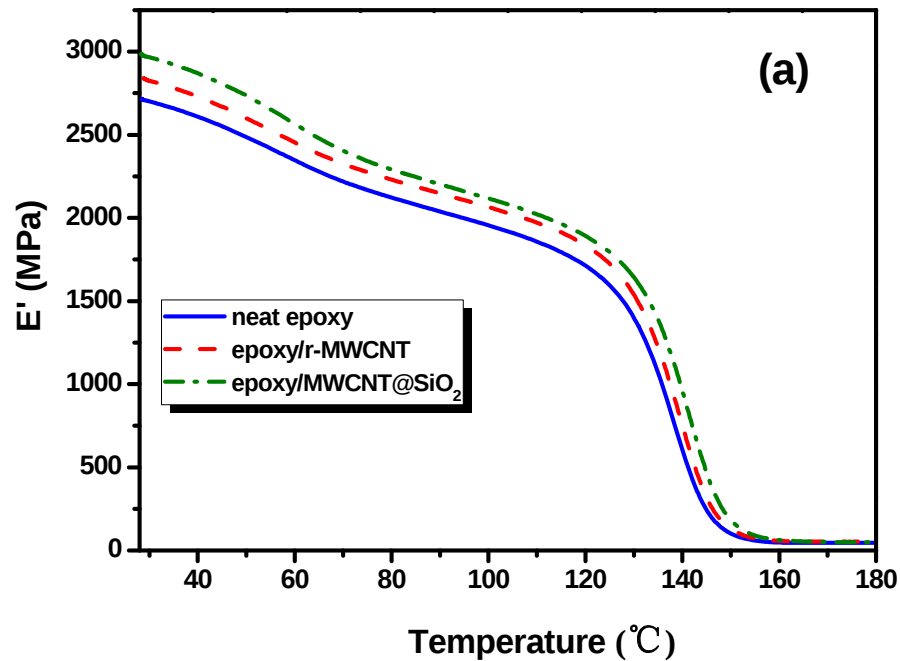
r-MWCNTs (a,b)



MWCNT@SiO₂ (c, d)



环氧树脂/MWCNT@SiO₂复合材料的DMA分析

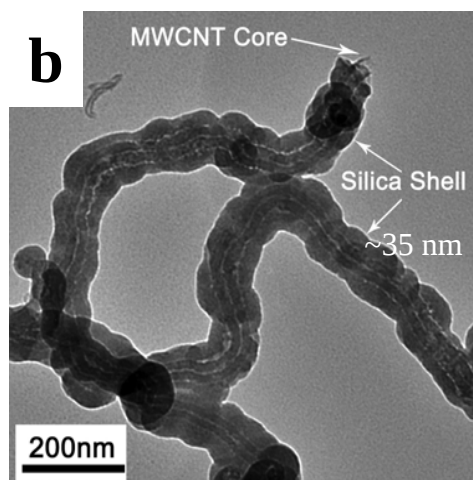
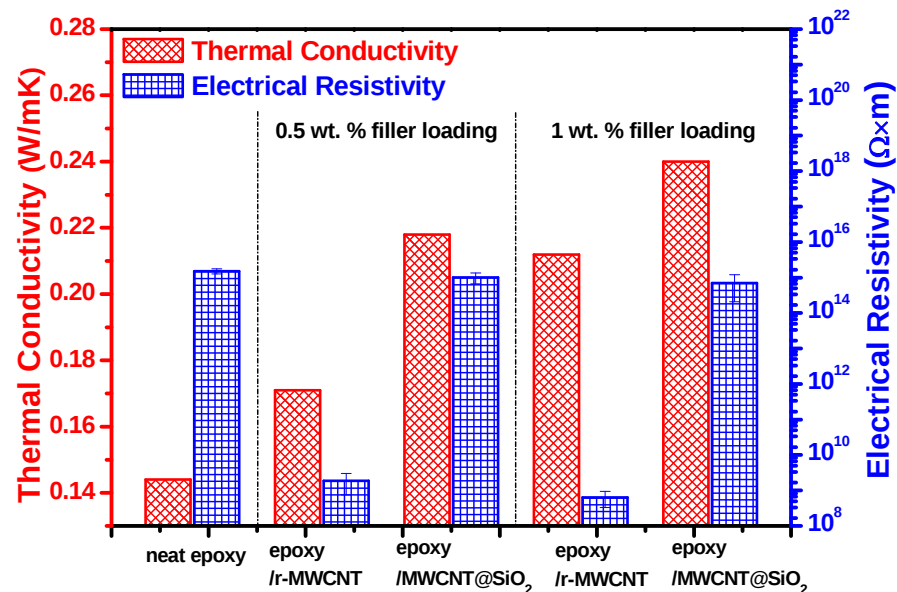
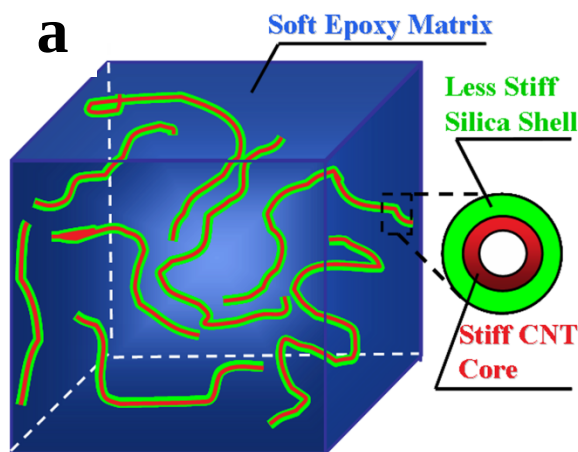


Xie X. L. et al, Carbon, 2011, 49, 495.

$E_{\text{环氧}}: 3 \text{ GPa}$

$E_{\text{SiO}_2}: \sim 70 \text{ GPa}$

$E_{\text{CNT}}: 0.6\text{-}1 \text{ TPa}$



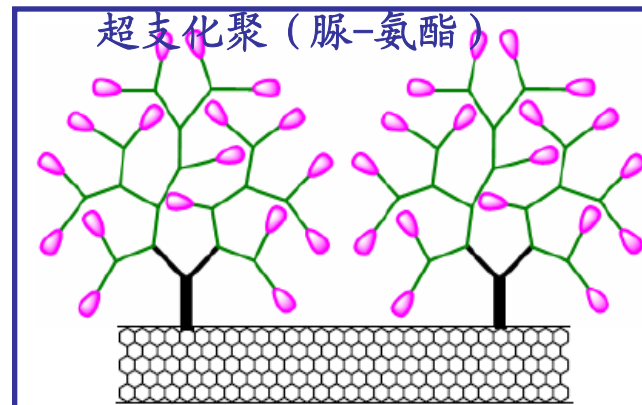
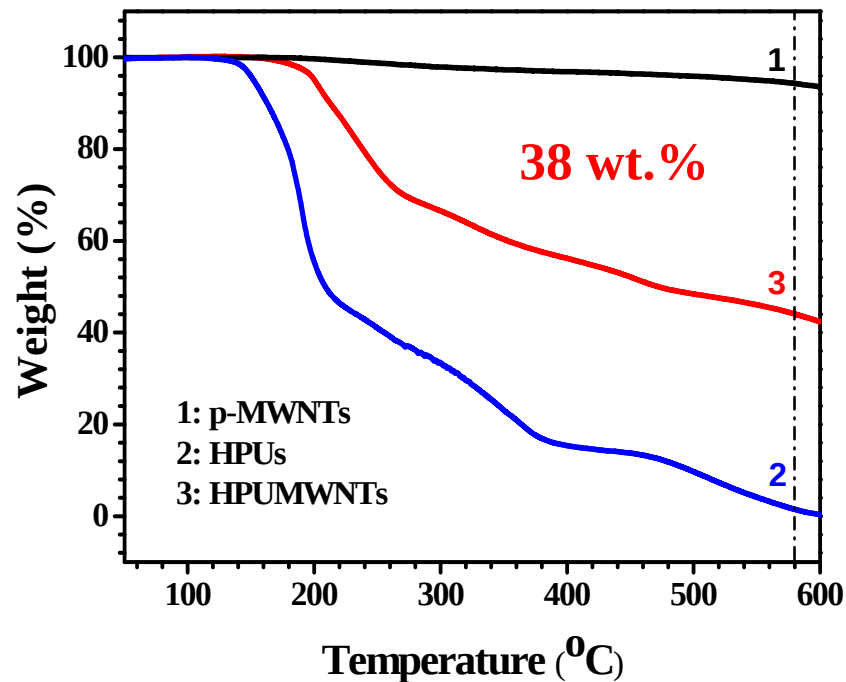
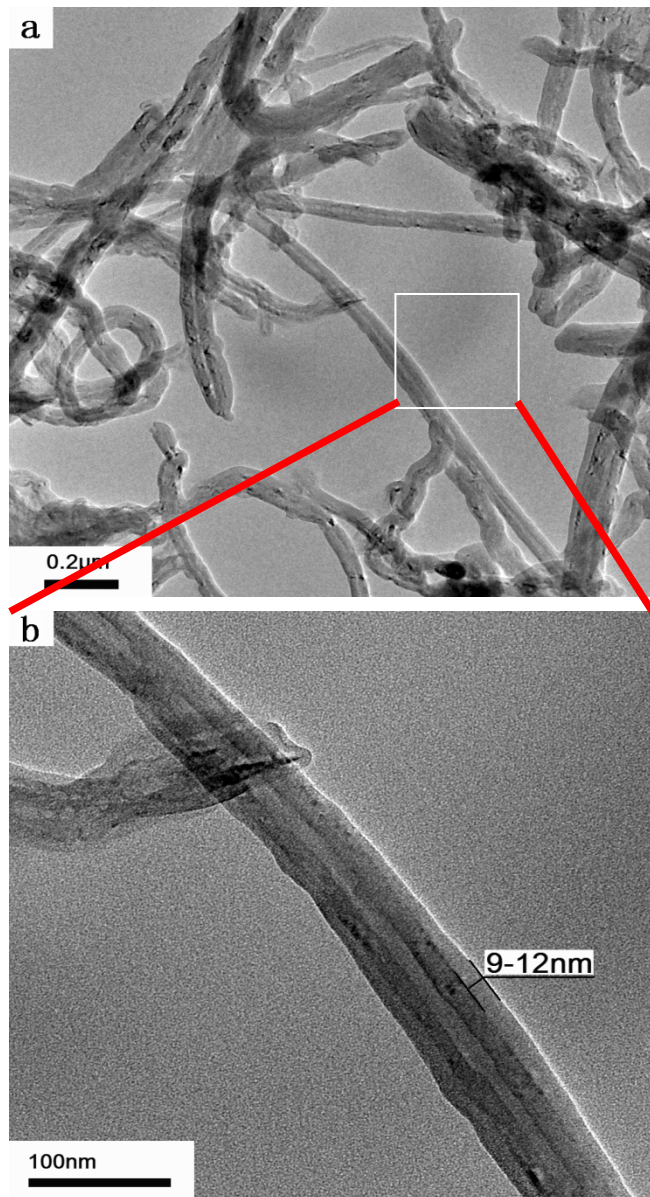
在多壁碳纳米管表面包覆二氧化硅，不仅赋予电绝缘性，还克服碳纳米管与聚合物基体的模量失配，促进界面声子共振耦合，显著提高了导热性能，克服了导热与电绝缘的矛盾

解孝林等, 中国发明专利ZL200910062796.2

Xie XL*, et al. *Carbon* 2011, 49: 495

Xie XL*, et al. *J. Mater. Sci.* 2009, 44: 4539

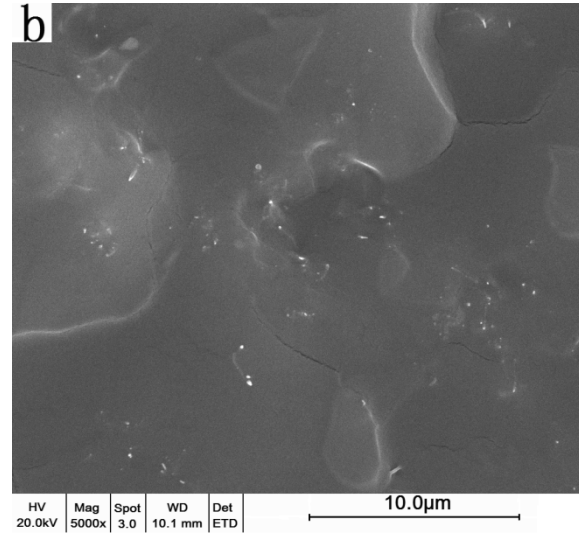
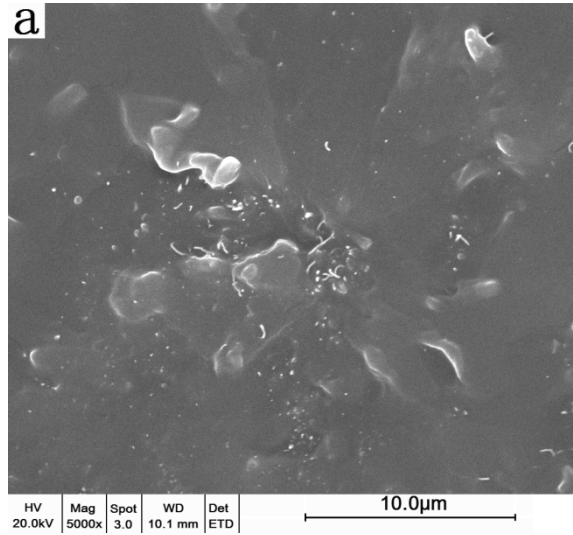
体系二：超支化聚(脲-氨酯)-g-MWNTs



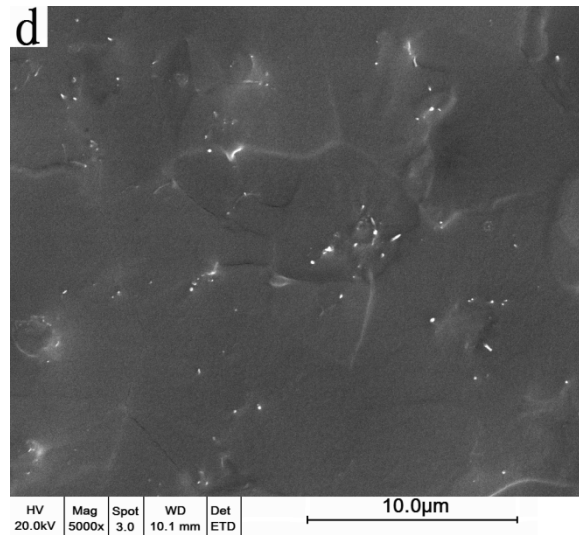
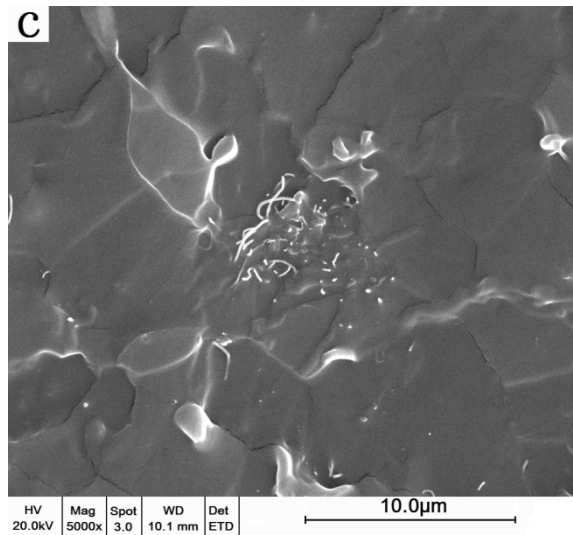
Xie Xiaolin*, et al, *Macromolecules* 2007, 40: 5858
Xie X. L. *, et al, *Macromol. Rapid Comm.* 2006, 27: 1695

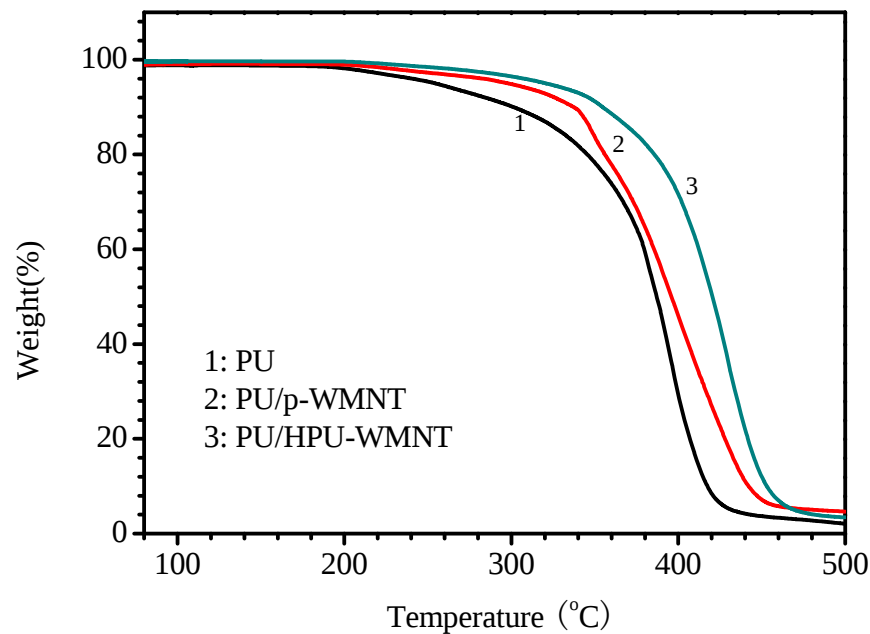
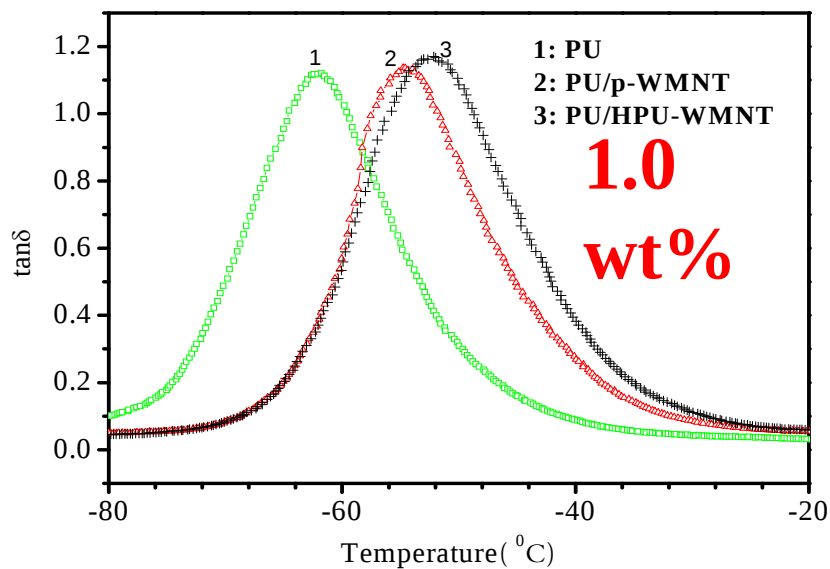
PU/HPU-MWNTs复合材料的断面形貌

0.5 wt%



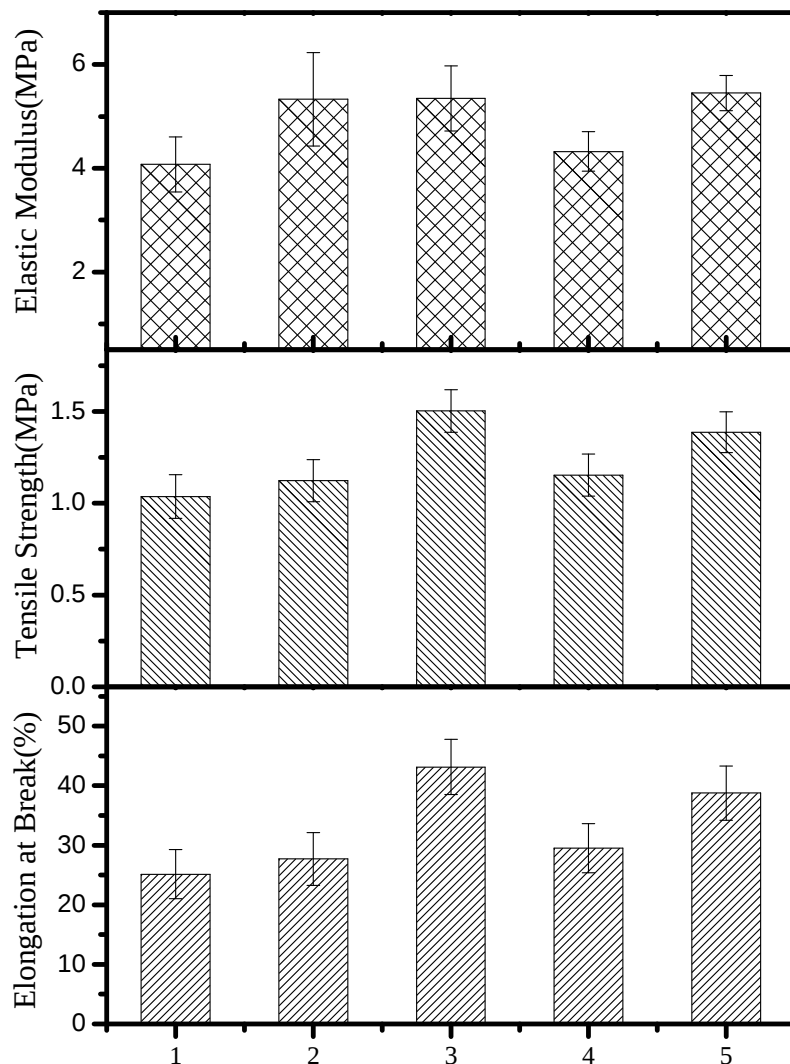
1.0 wt%





Samples	T_g ($^{\circ}\text{C}$)	$T_{5\%}$ ($^{\circ}\text{C}$)	T_{max} ($^{\circ}\text{C}$)
Neat PU	-61.9	254.4	395.7
PU/p-MWNT (1%)	-54.7	298.0	403.9
PU/HPU-MWNT (1%)	-52.1	320.6	430.2

PU/HPU-MWNTs 的热行为分析



1: neat PU;

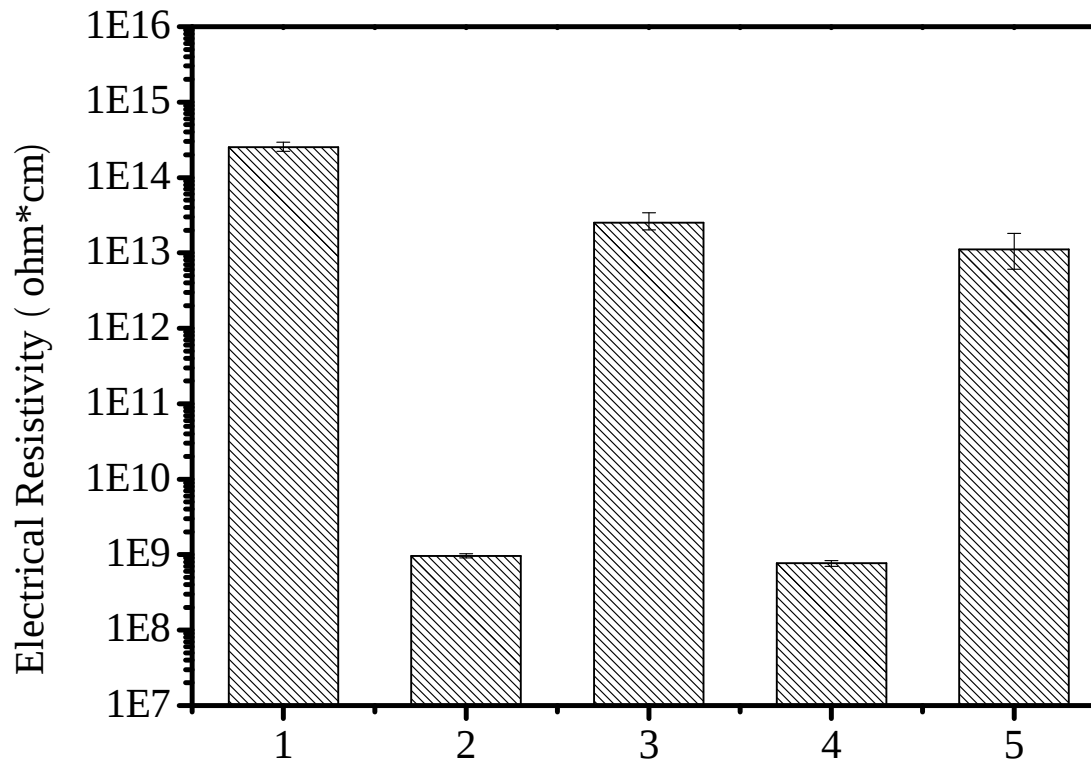
2: PU/p-MWNT (0.5 wt%);

3: PU/HPU-MWNT(0.5 wt%);

4: PU/p-MWNT (1 wt%);

5: PU/HPUs-MWNT (1 wt%)

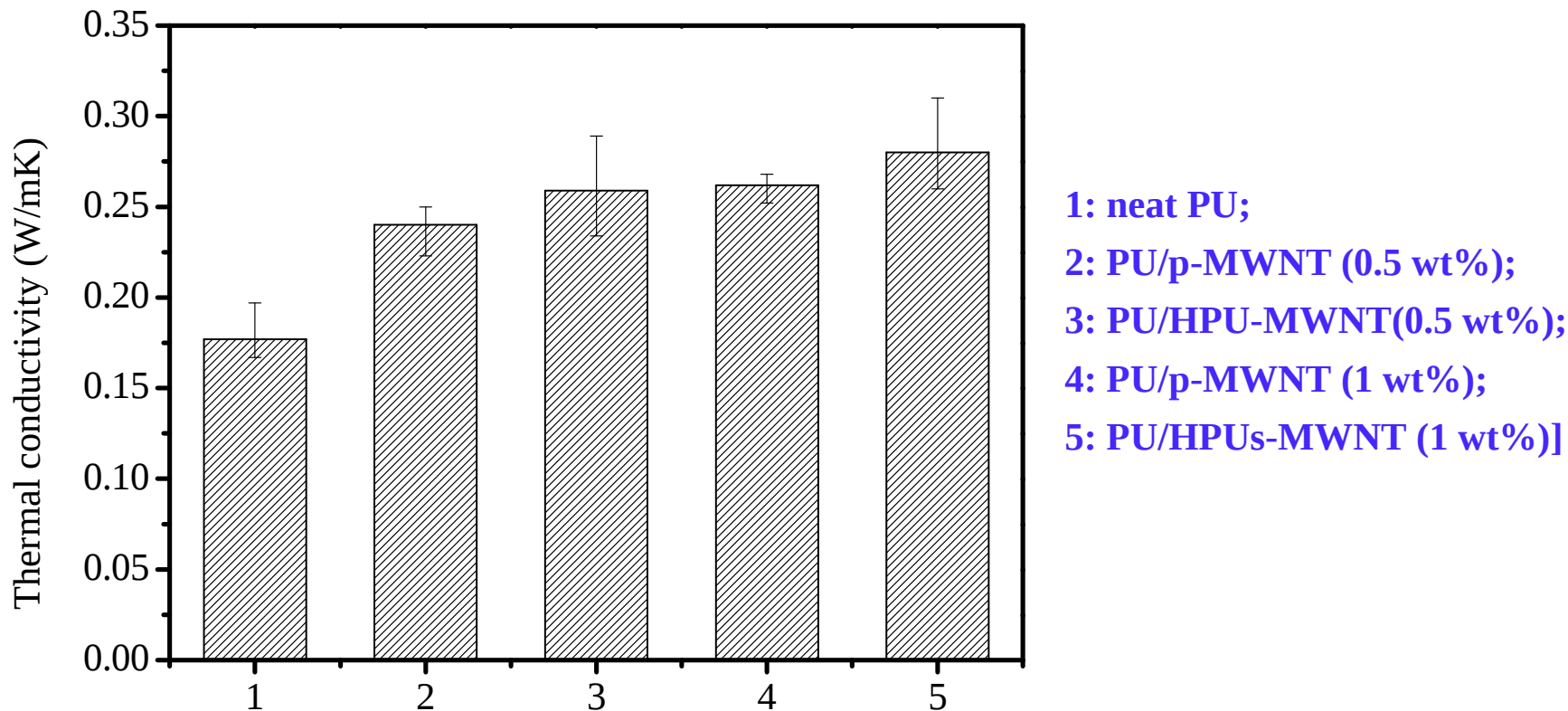
PU/HPU-MWNTs 的力学性能



1: neat PU; 2: PU/p-MWNT (0.5 wt%);

3: PU/HPU-MWNT(0.5 wt%); 4: PU/p-MWNT (1 wt%);

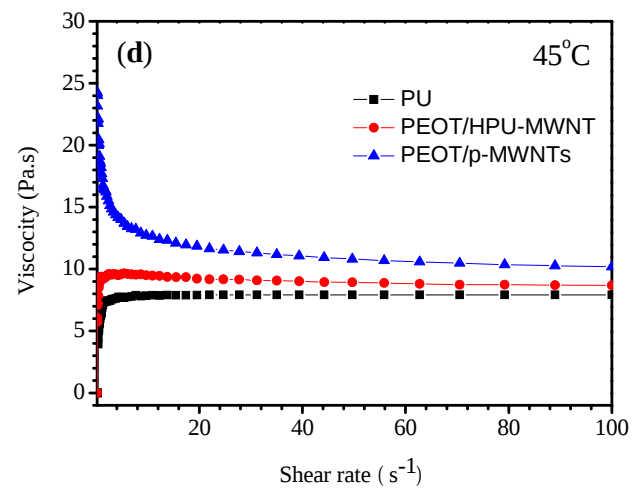
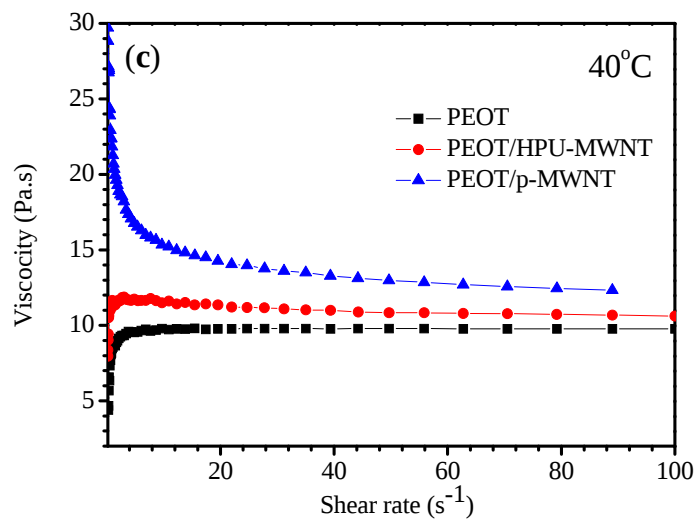
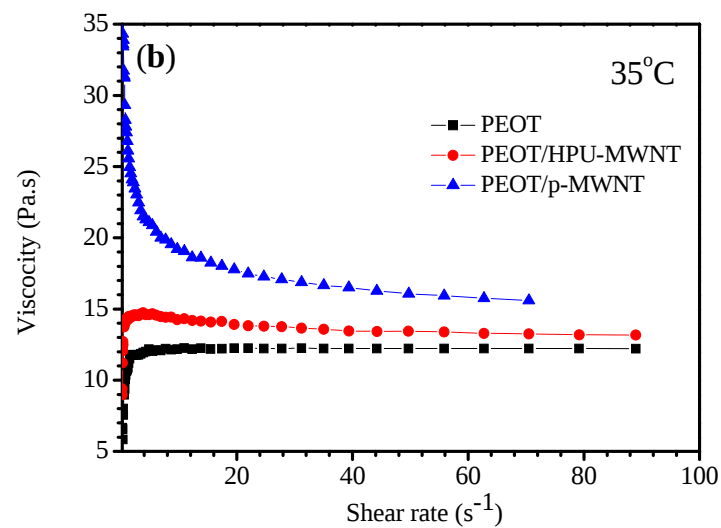
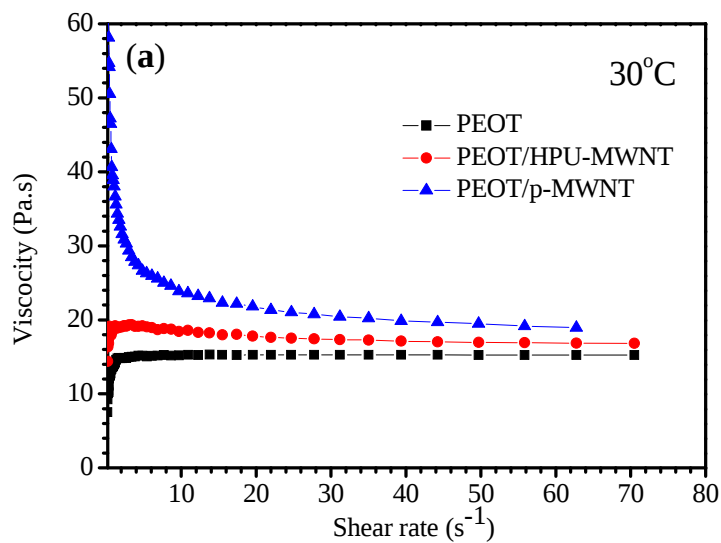
5: PU/HPUs-MWNT (1 wt%)]



解孝林等，一种电绝缘导热聚氨酯复合材料及其制备方法. 中国专利 (申请号 200910273311.4)

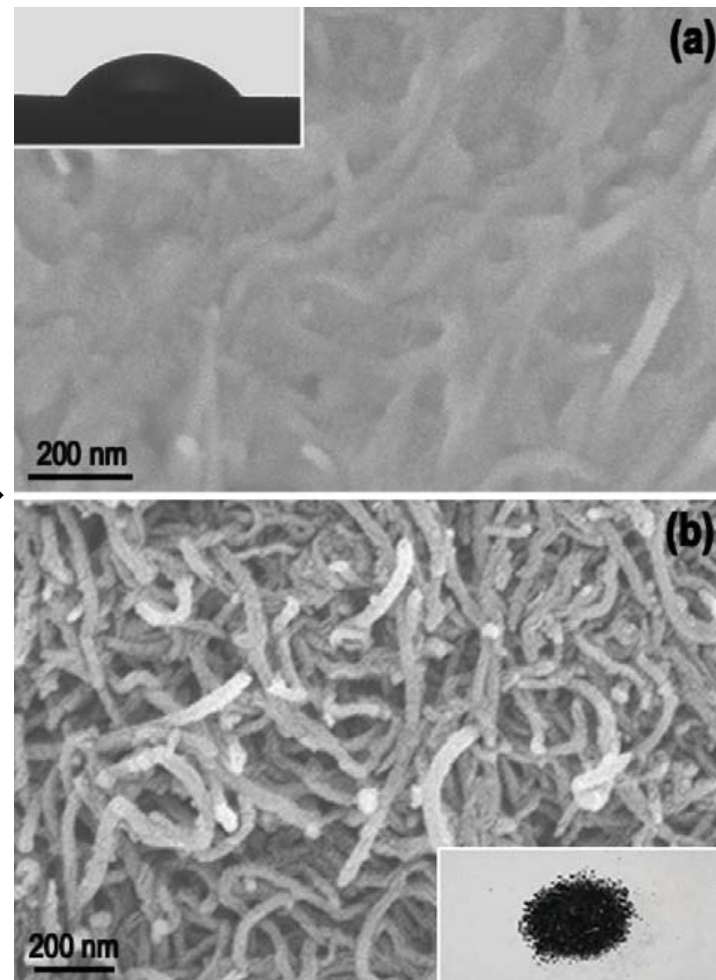
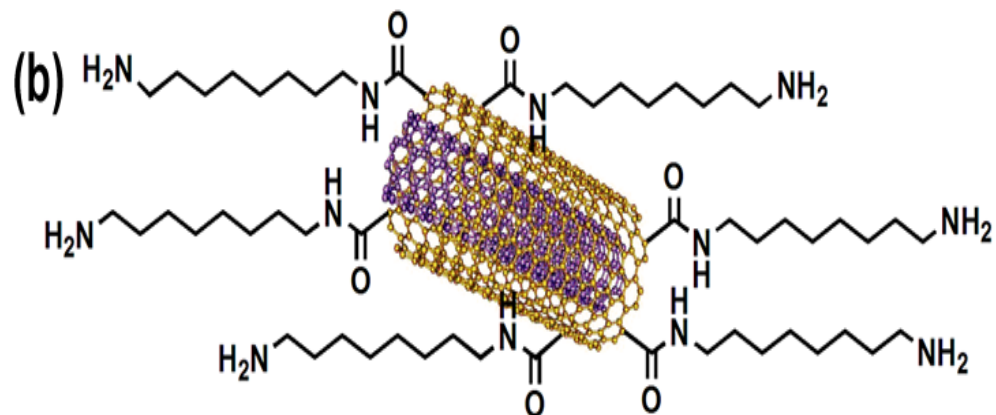
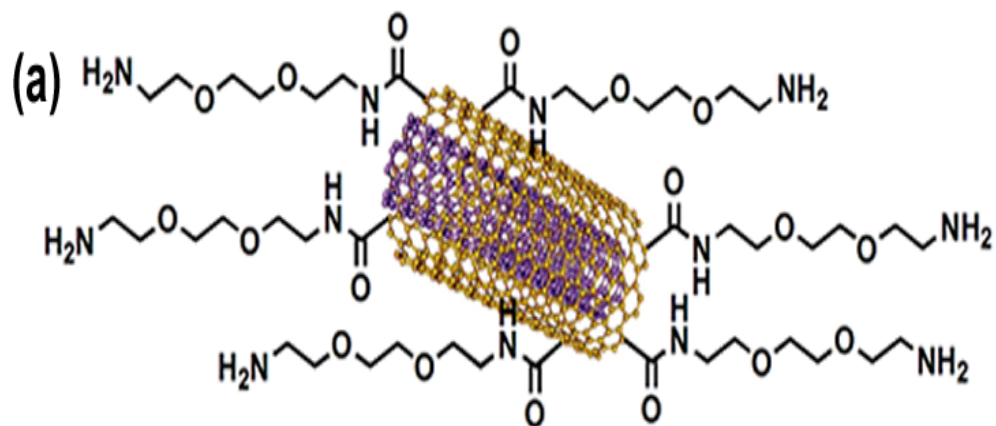
Xie X. L. *, *et al*, Enhanced properties of polyurethane/MWNT nanocomposites by surface grafting modification of hyperbranched poly(urea-urethane) on MWNTs . *Compos. B*, 2011, 42(8): 2111-2116.

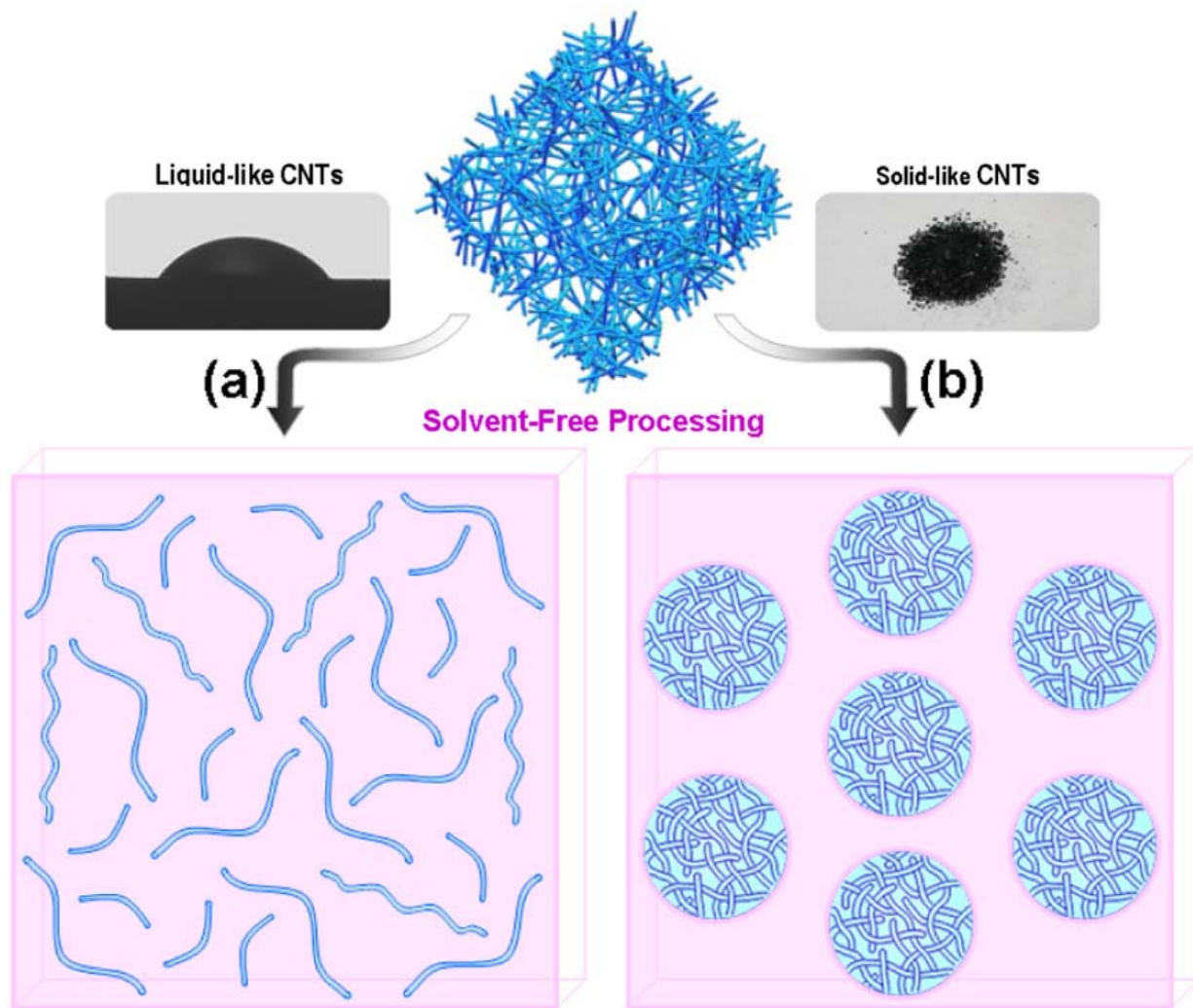
PU/HPU-MWNTs 的导热性能



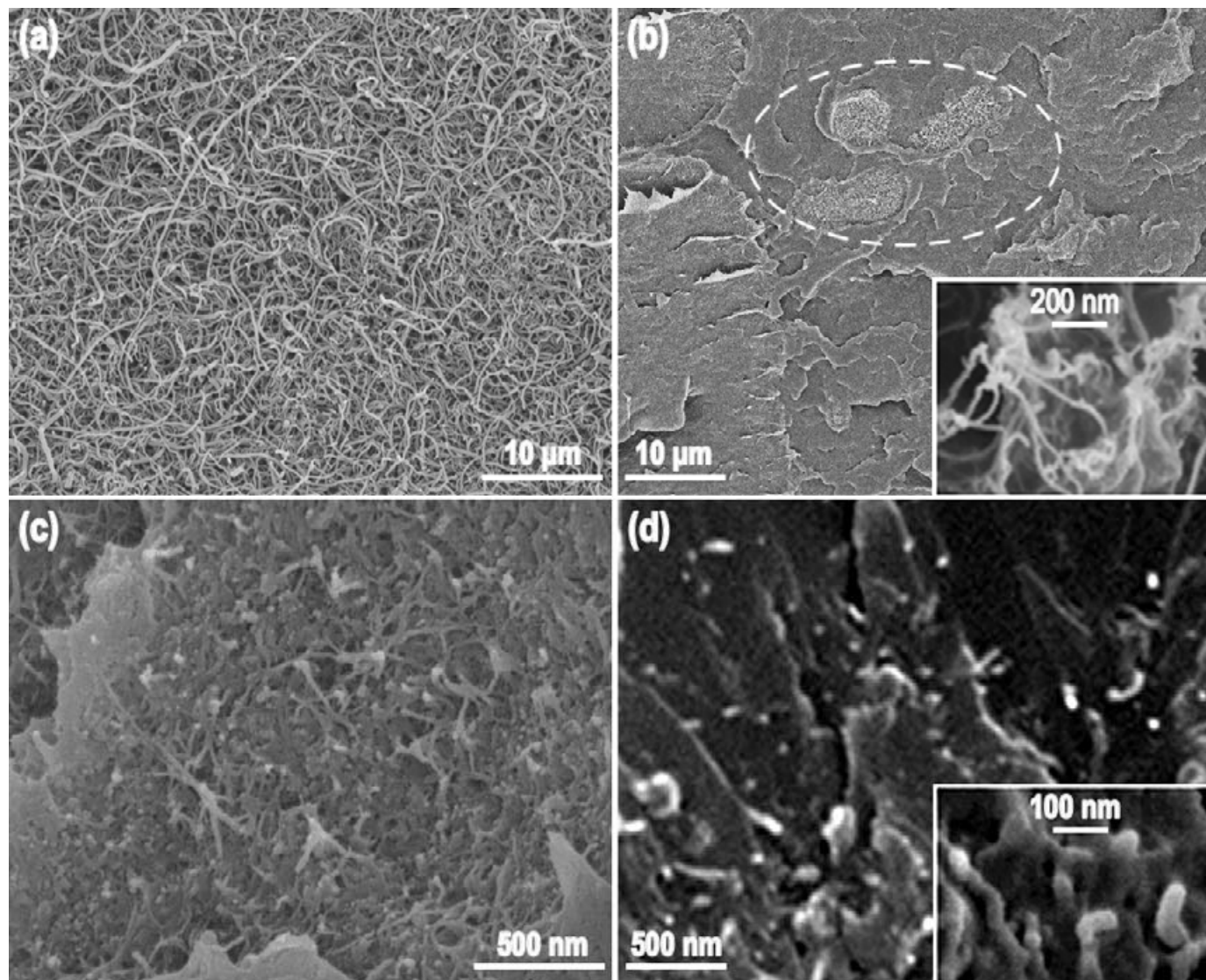
PU/HPU-MWNTs 预聚物的流变性质

体系三：类流体碳管



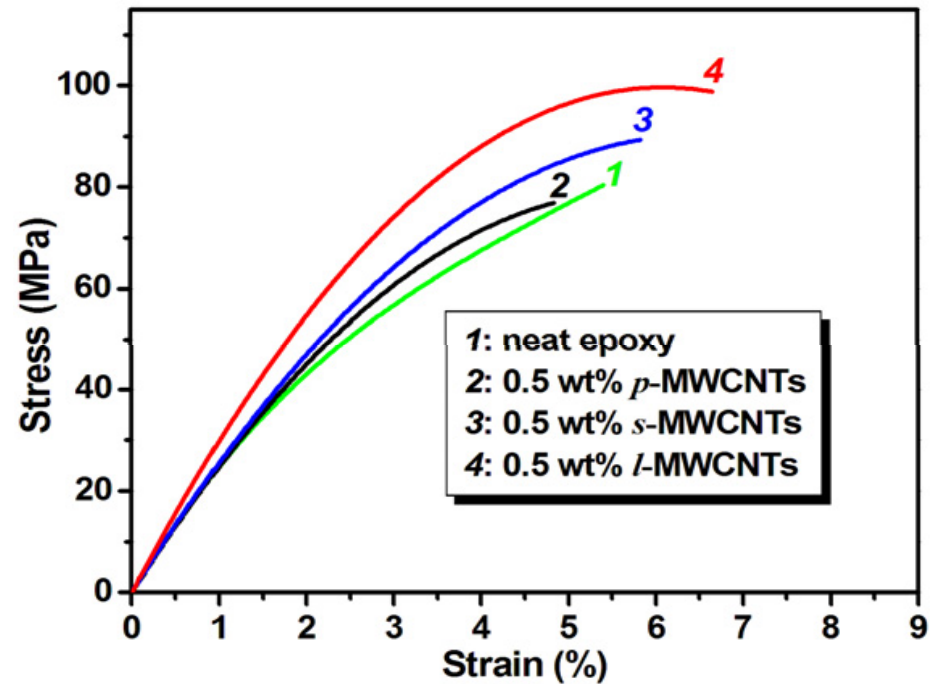
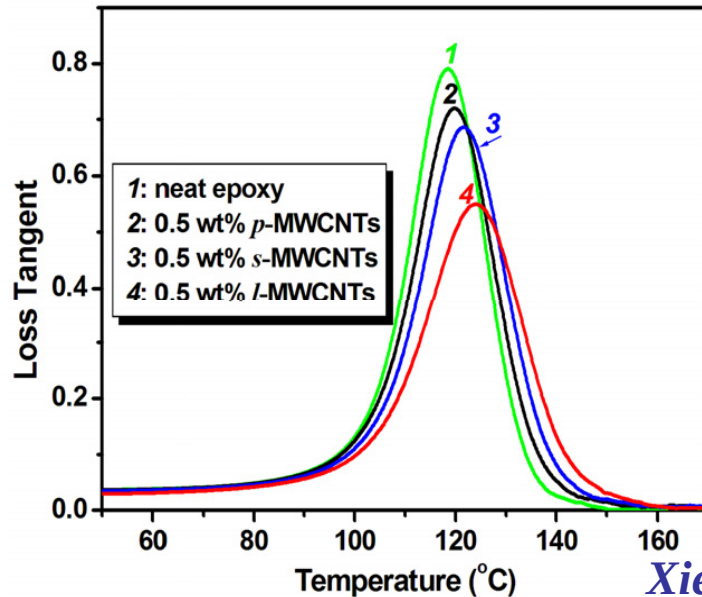
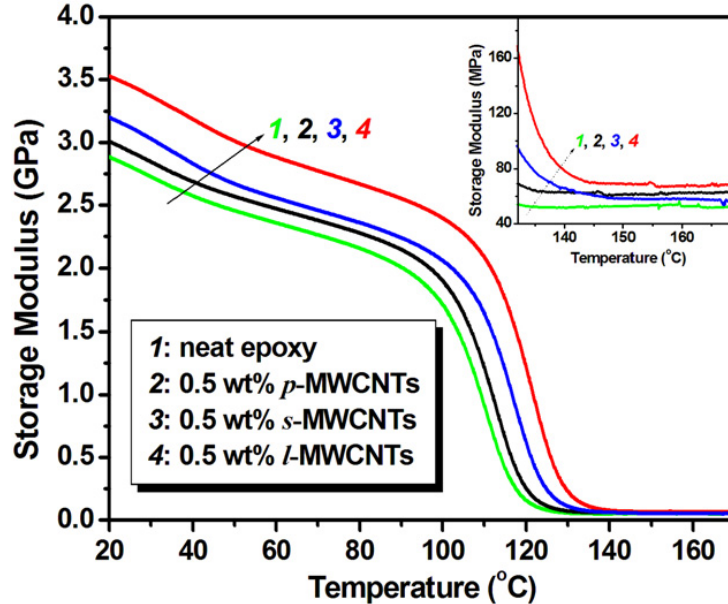


类流体碳管/环氧复合材料SEM图



0.5
wt%

类流体碳管/环氧复合材料性能



导热性纳米粒子类流体化设计

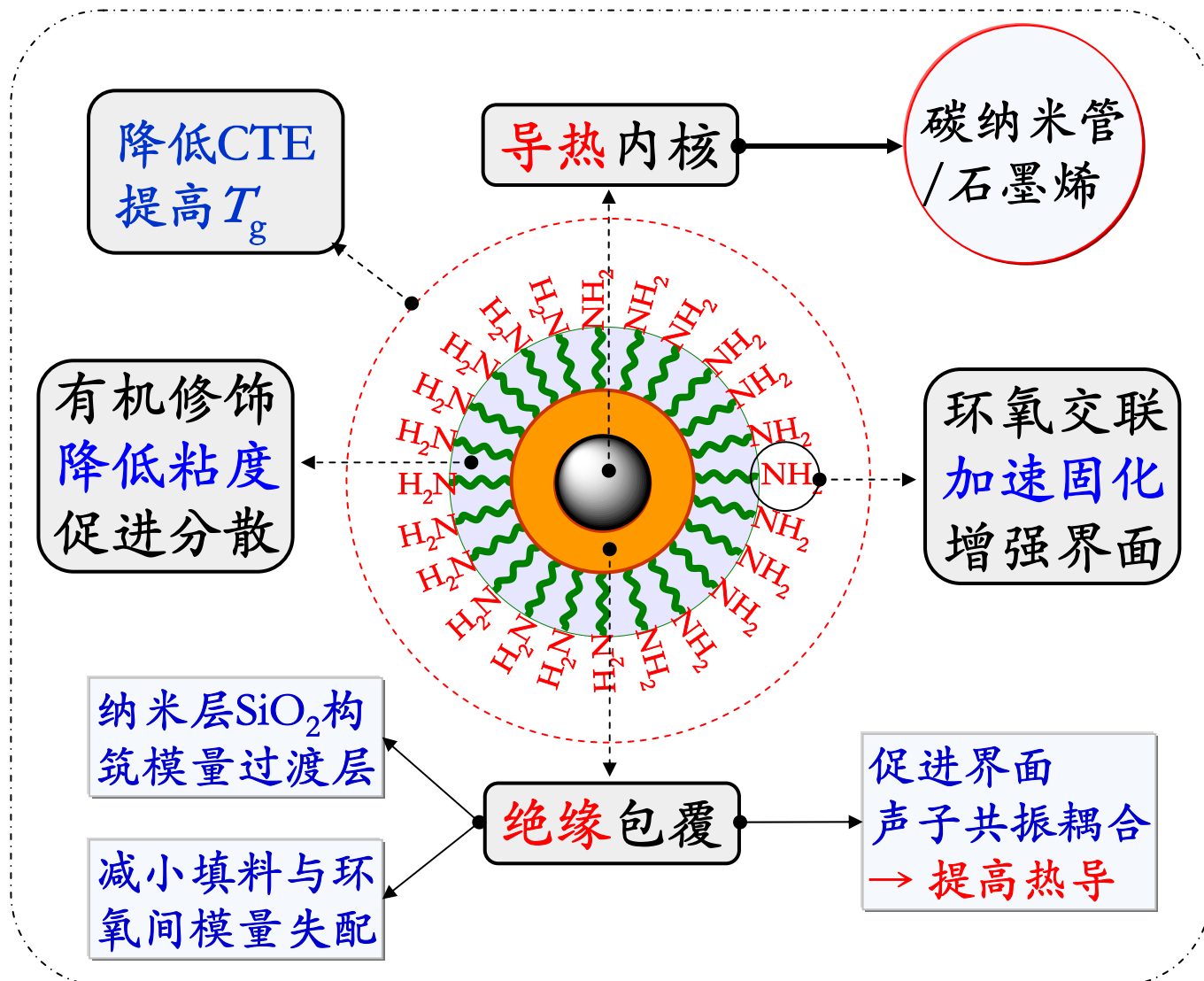
固态



改性



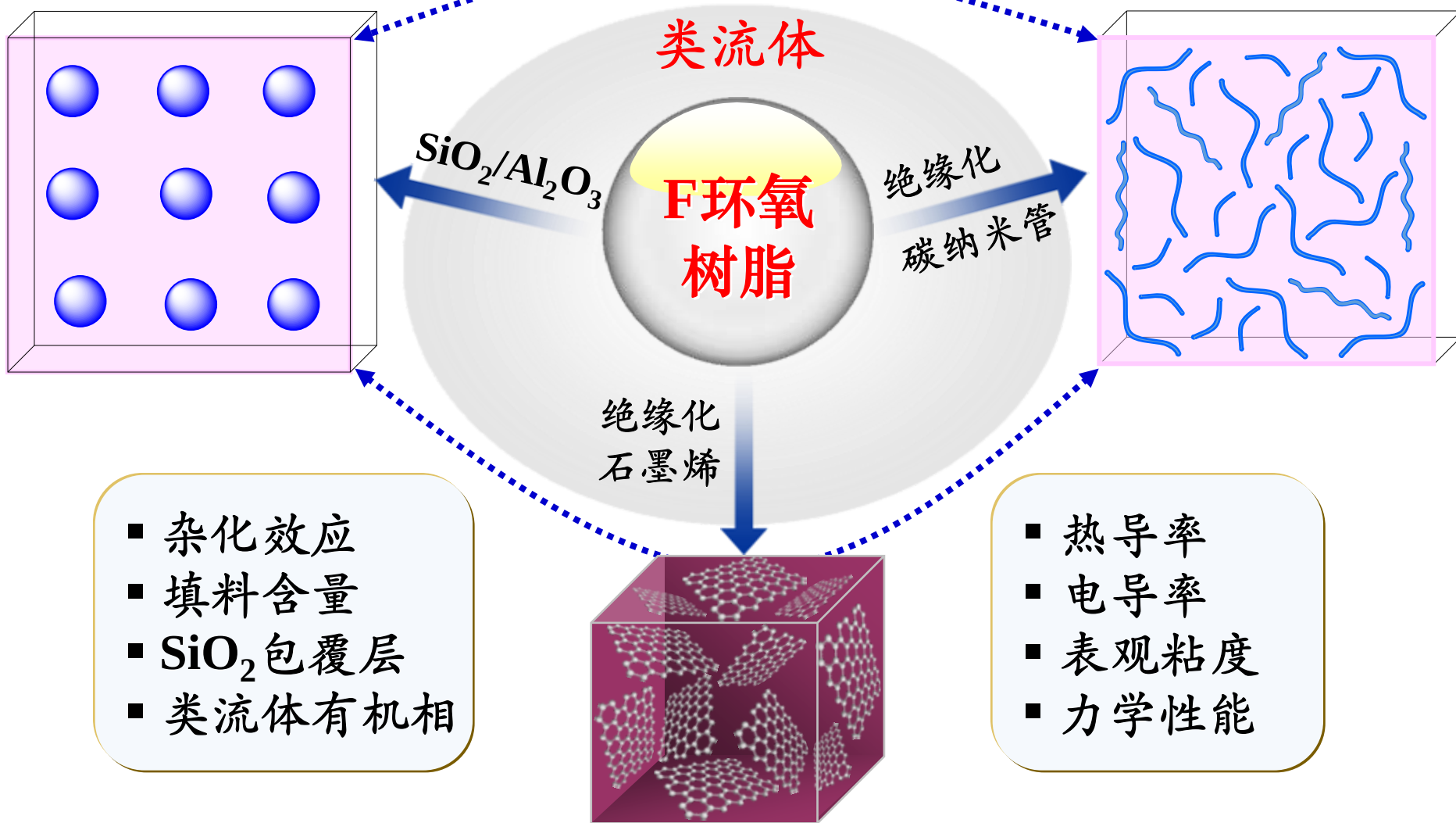
类流体
可流动
易加工



环氧树脂复合材料制备

杂化效应是调控界面模量匹配、增强界面作用，减小界面热阻。

多元杂化



致谢

同事：解孝林、廖永贵、杨应奎

学生：崔 伟、赵瑾朝、侯金星

国家自然科学基金委员会

科技部

教育部

华中科技大学

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